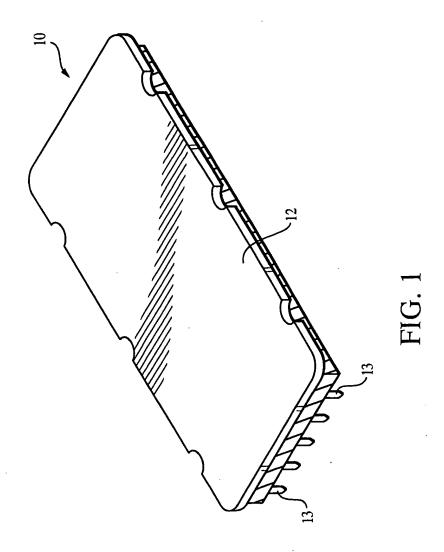
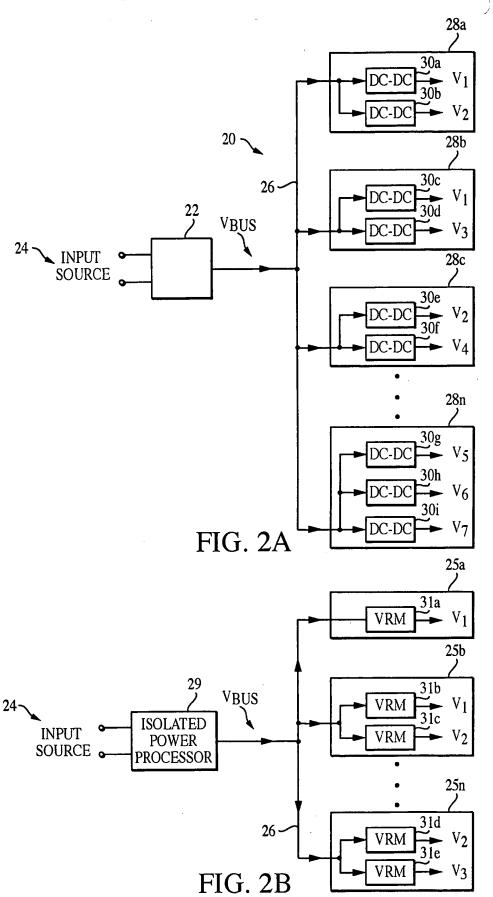
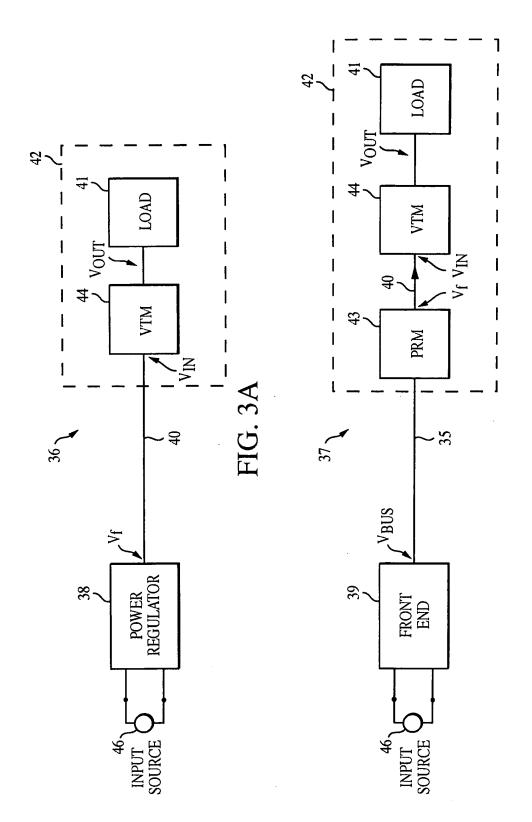
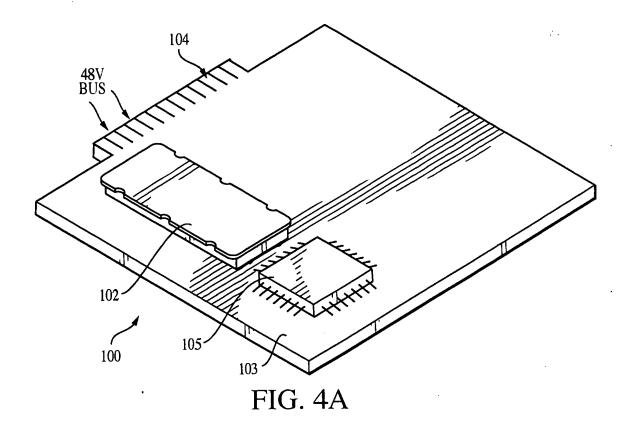
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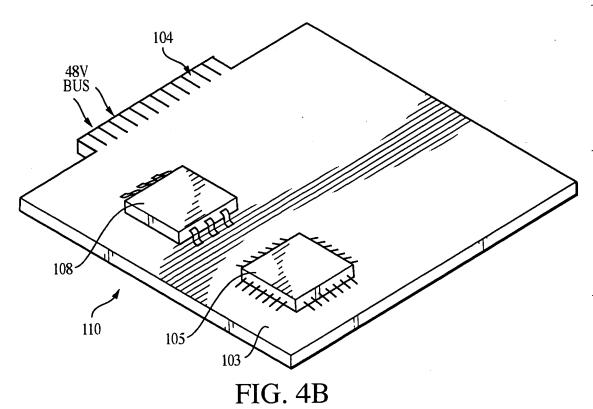




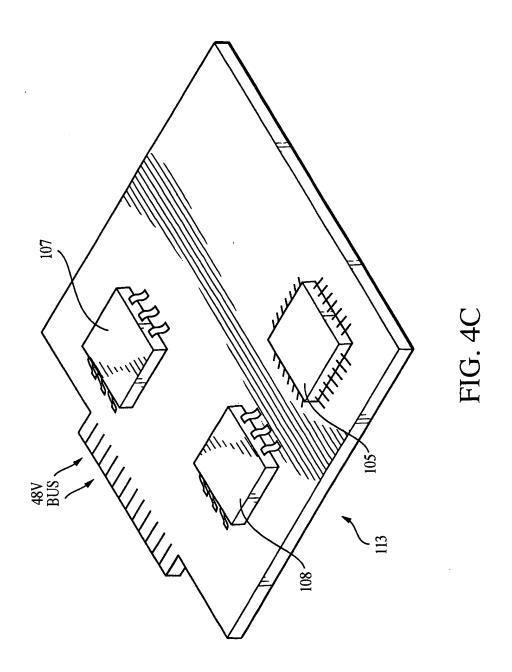


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Matter No.: 00614-136004

Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER

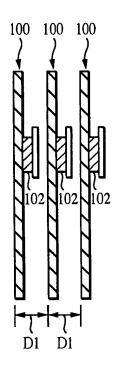


FIG. 5A

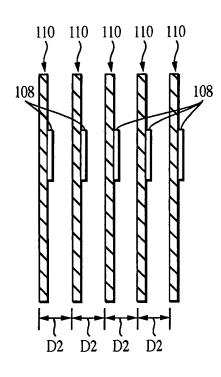
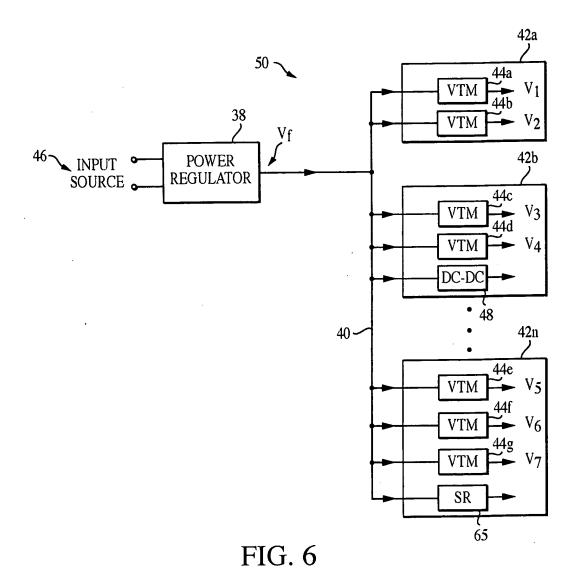


FIG. 5B

Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER



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Matter No.: 00614-136004 Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

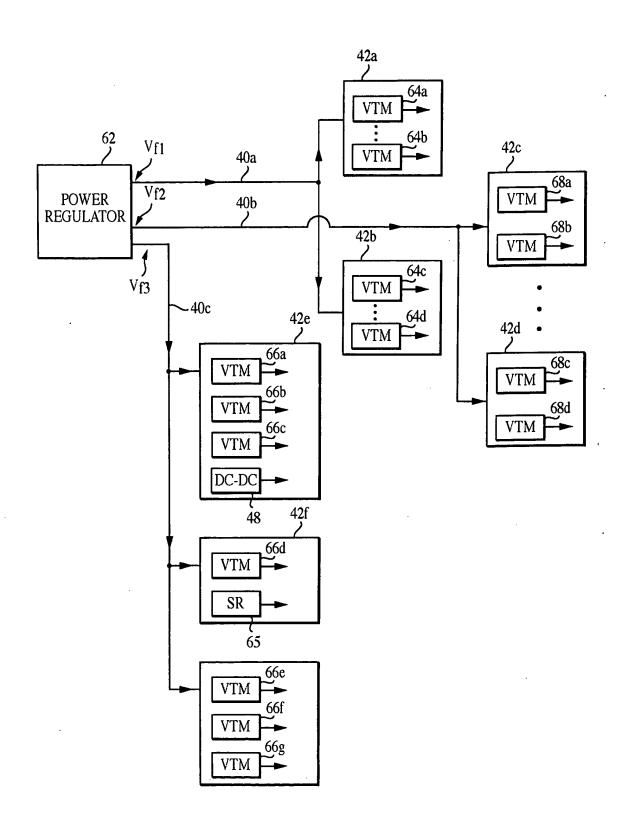


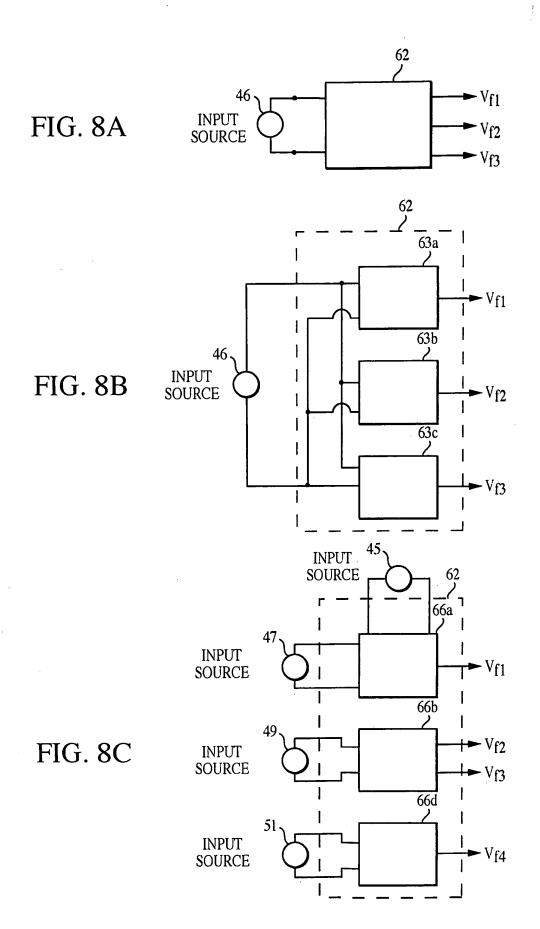
FIG. 7

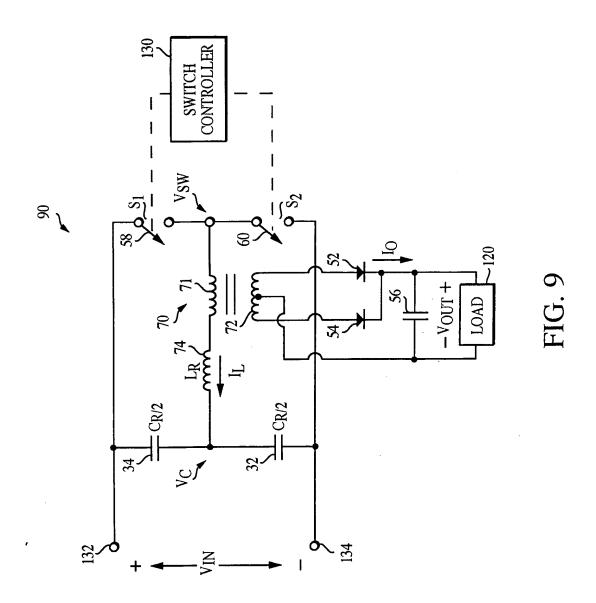
Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

CONVERTERS

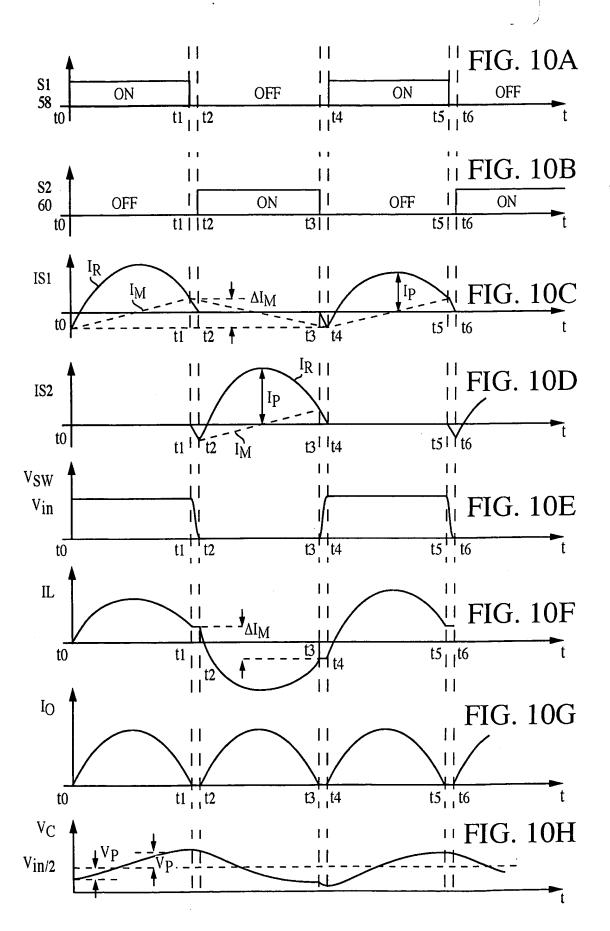
ţ





Matter No.: 00614-136004 Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER



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Matter No.: 00614-136004

Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER

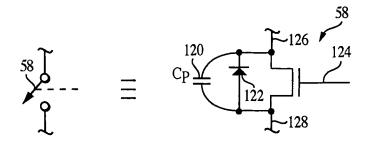


FIG. 11

OUTPUT RESISTANCE MODULATION IN POWER CONVERTERS

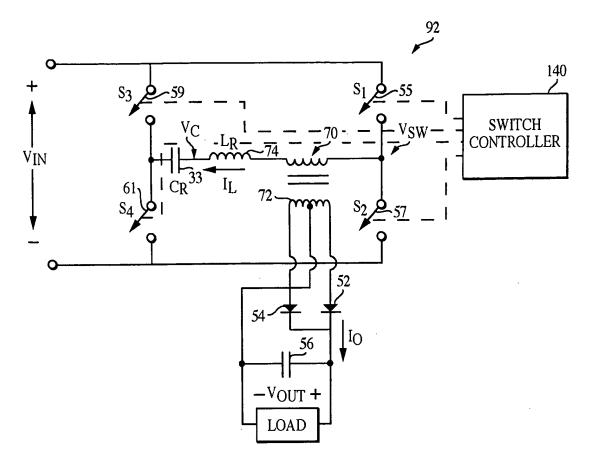


FIG. 12

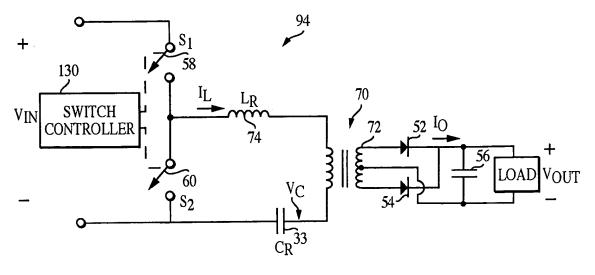


FIG. 13

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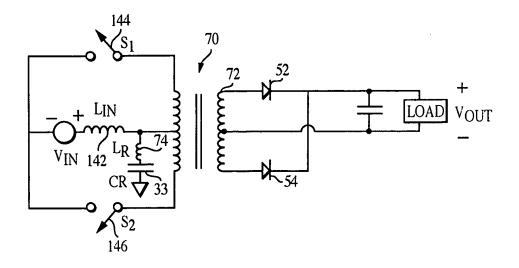


FIG. 14

Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER

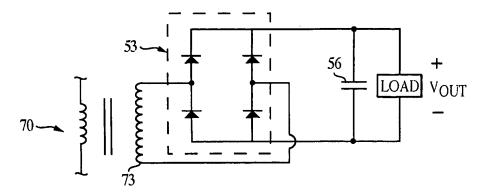


FIG. 15A

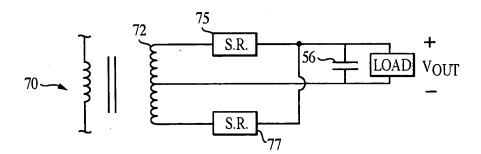
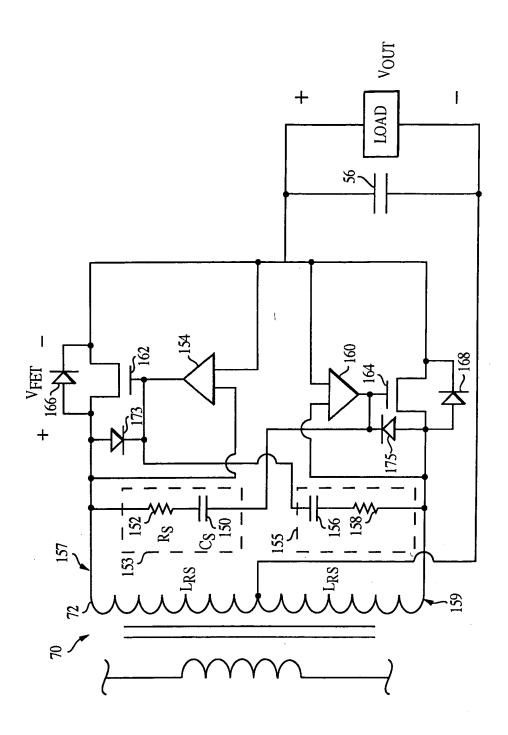
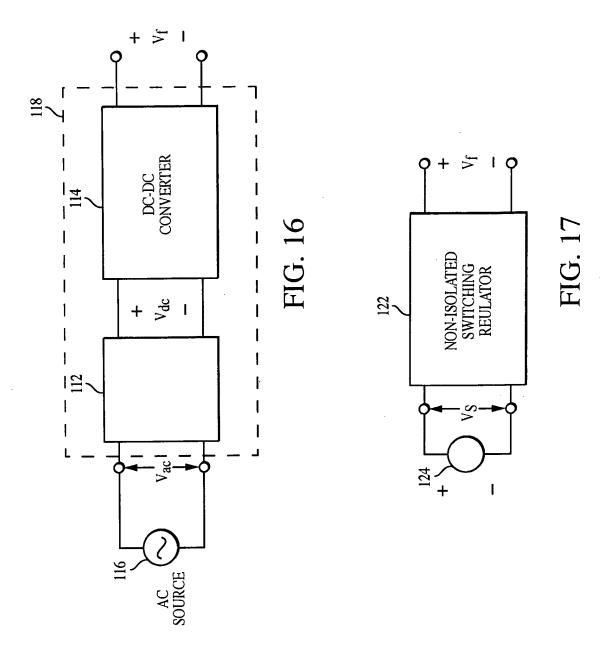
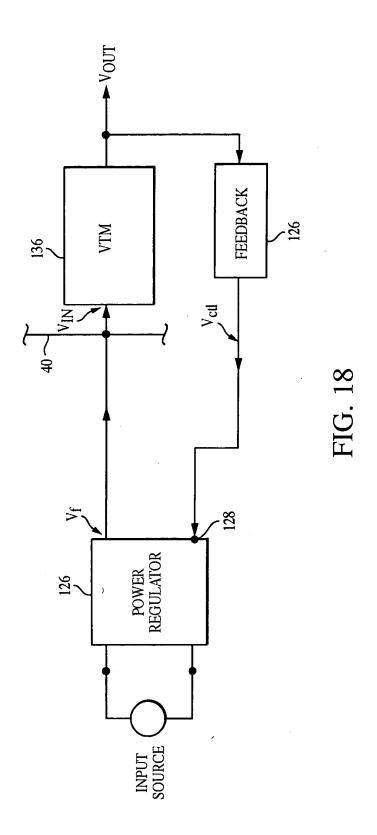
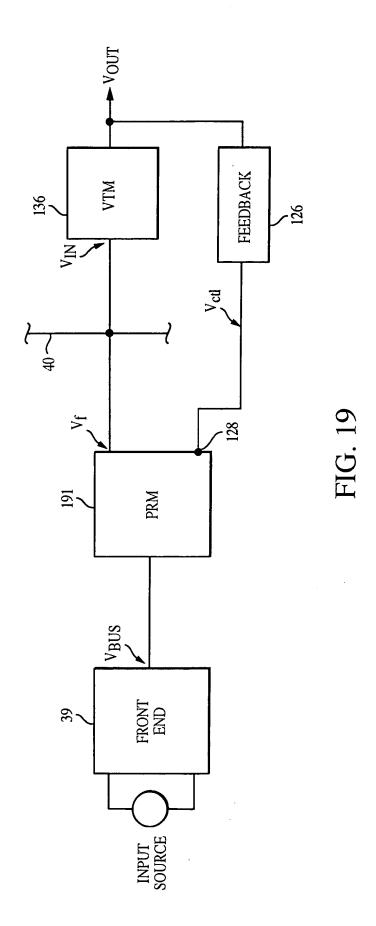


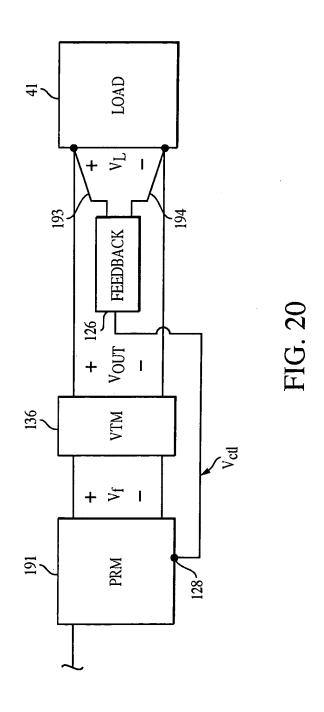
FIG. 15B











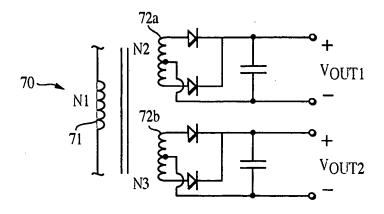
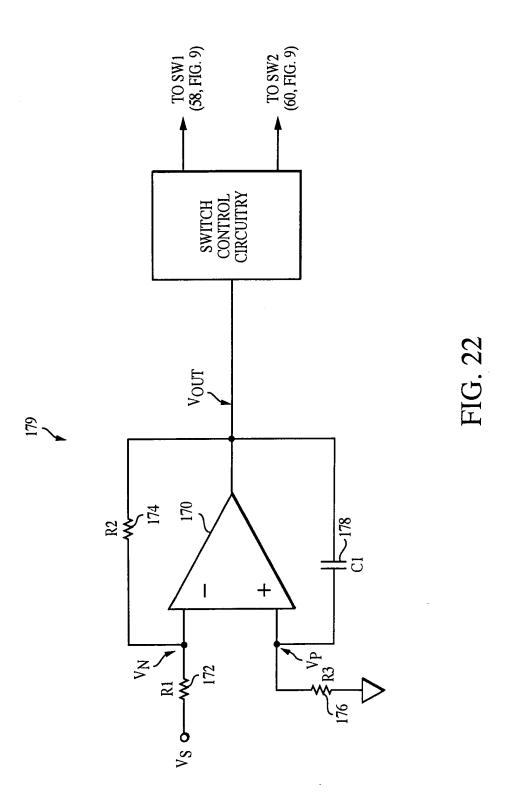
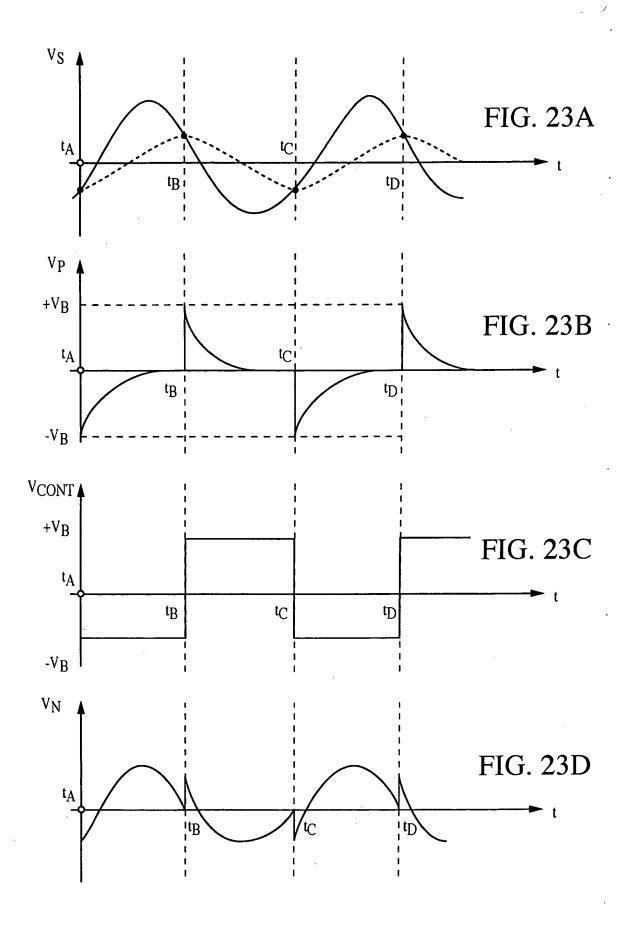


FIG. 21



Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER



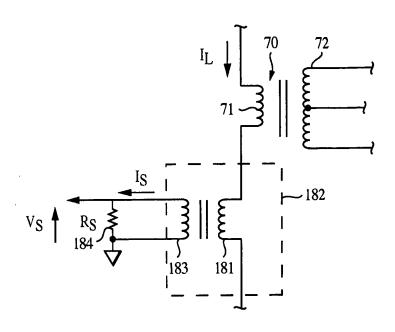
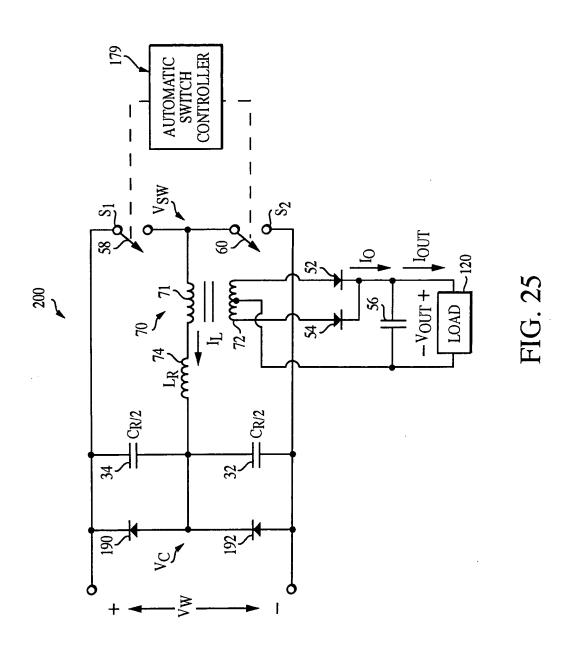


FIG. 24



Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

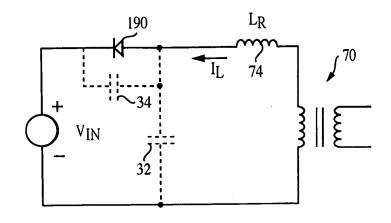
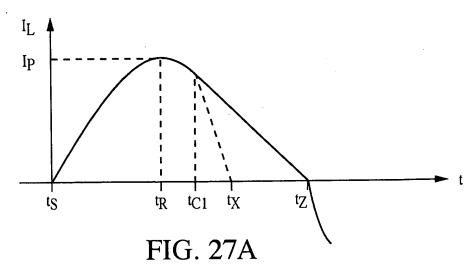


FIG. 26



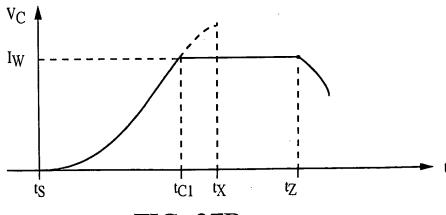


FIG. 27B

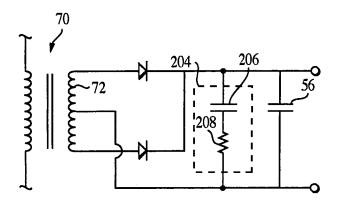


FIG. 28

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Matter No.: 00614-136004 Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

CONVERTERS

BUCK REGULATED SINE AMPLITUDE CONVERTER

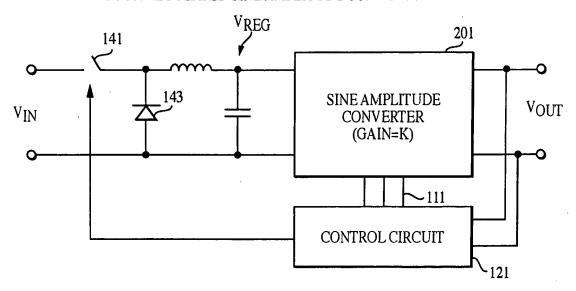


FIG. 29A

BOOST REGULATED SINE AMPLITUDE CONVERTER

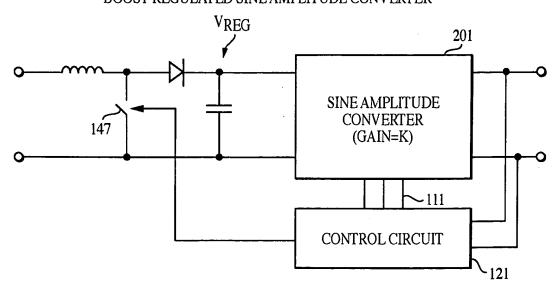
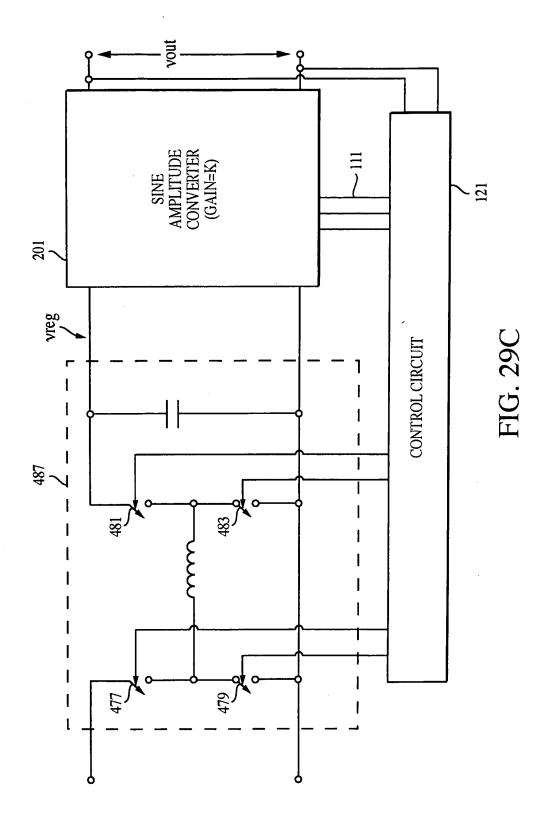


FIG. 29B



Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER
CONVERTERS

BOOT STRAP REGULATED DC-DC CONVERTER

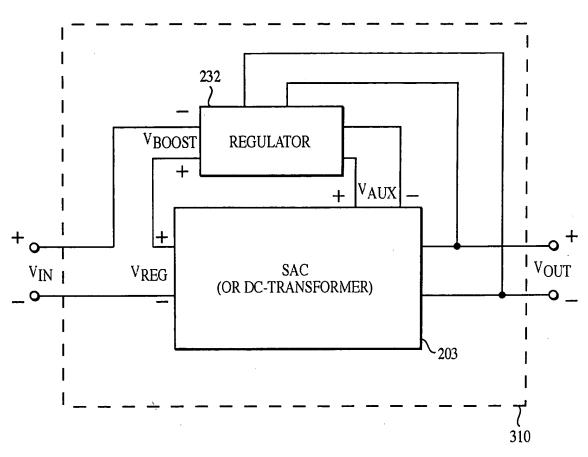


FIG. 30

Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER
CONVERTERS

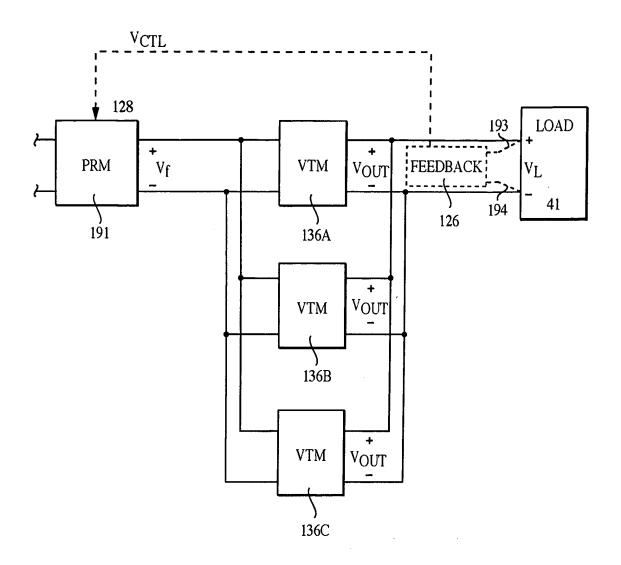
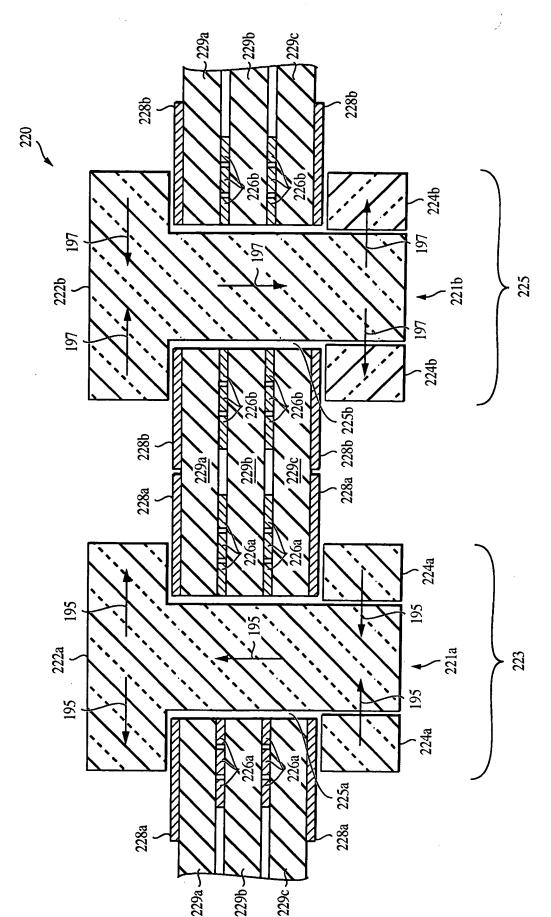


FIG. 31



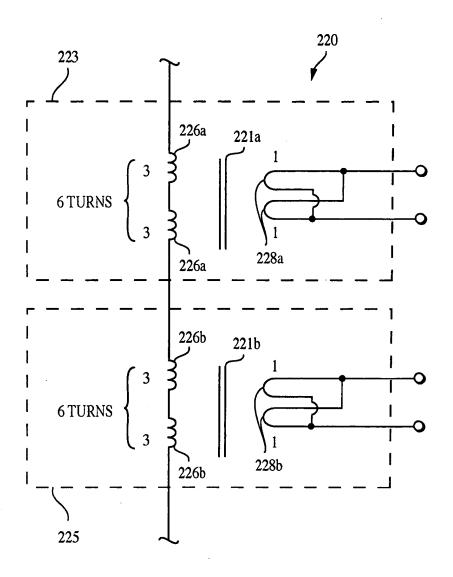


FIG. 33

Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER

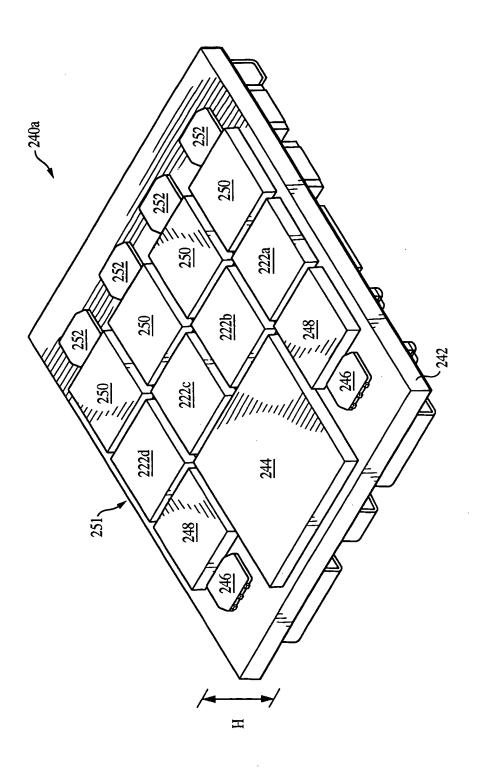
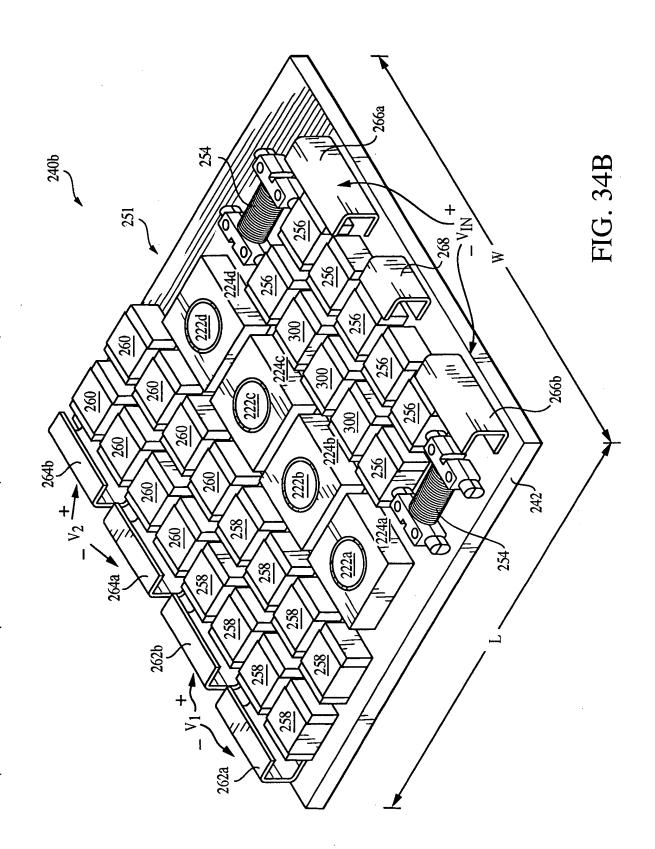


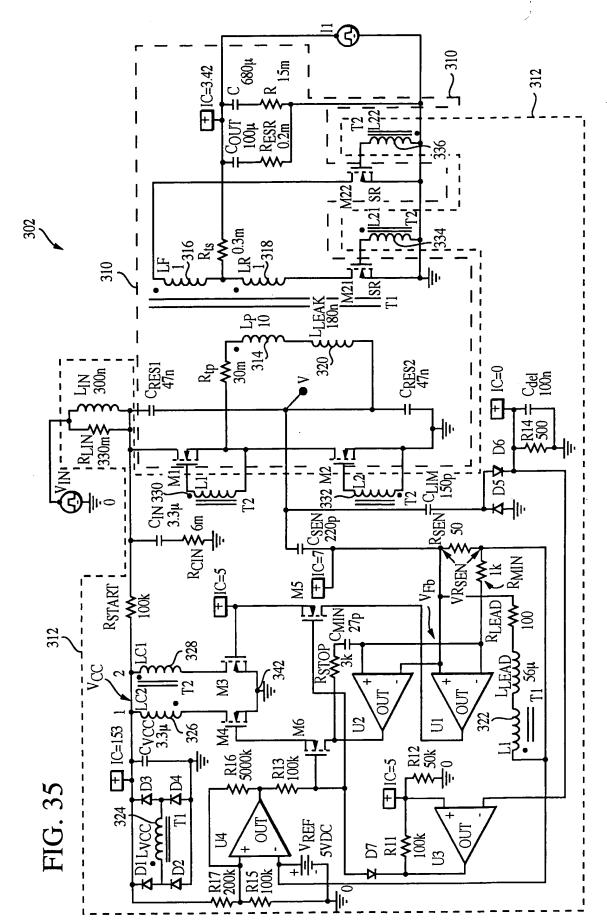
FIG. 34A

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Matter No.: 00614-136004 Pag Applicant(s): Patrizio Vinciarelli OUTPUT RESISTANCE MODULATION IN POWER

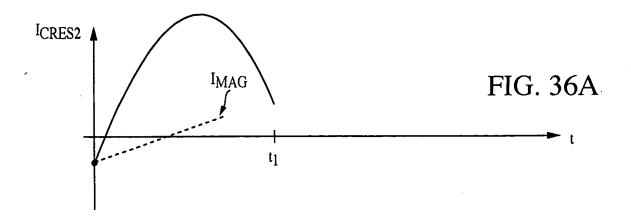


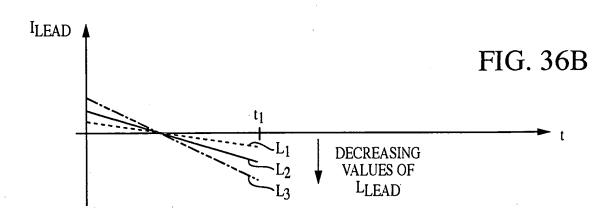
Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER

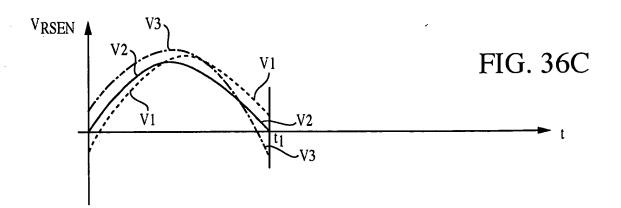


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Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER
CONVERTERS



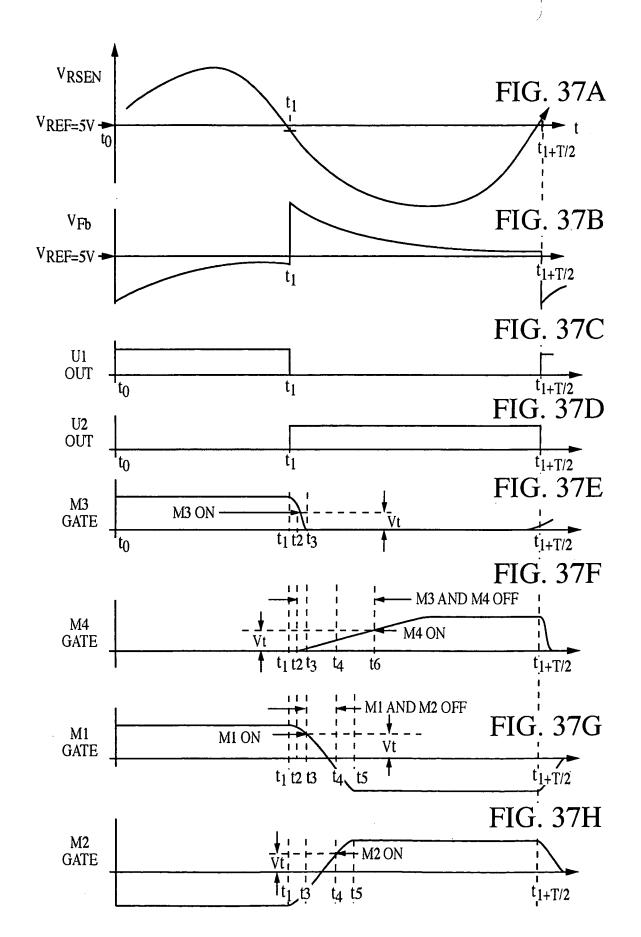




Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

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Matter No.: 00614-136004 Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

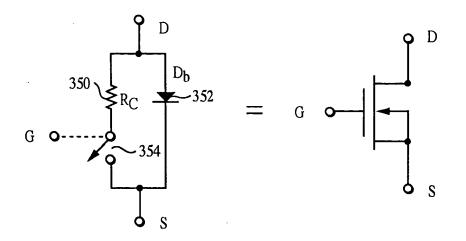


FIG. 38

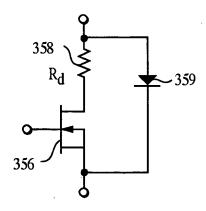


FIG. 39A

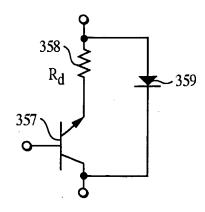
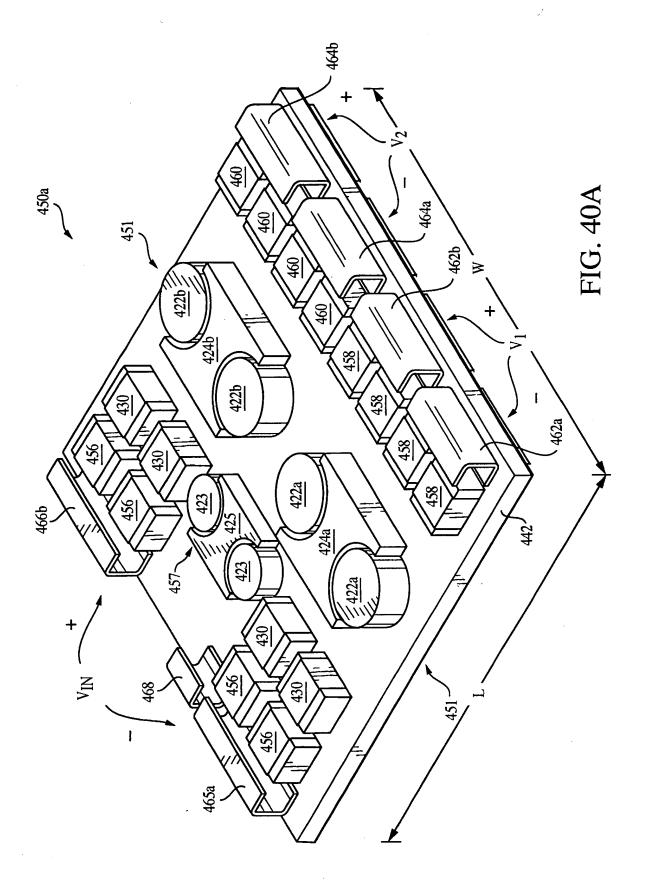
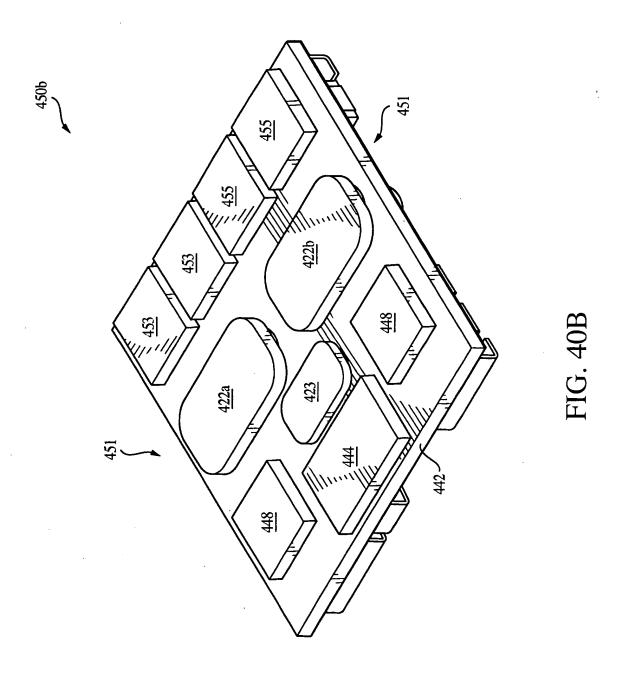


FIG. 39B

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Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER

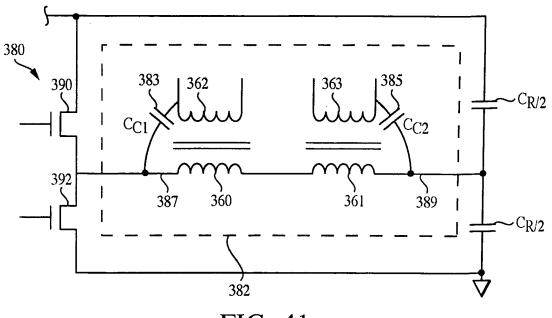


FIG. 41

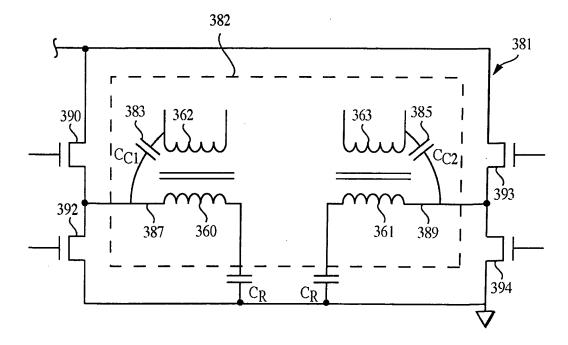


FIG. 42

Matter No.: 00614-136004 Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

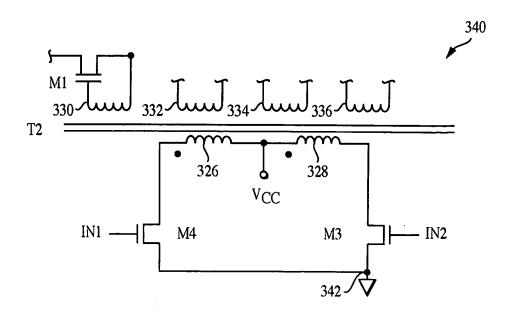


FIG. 43

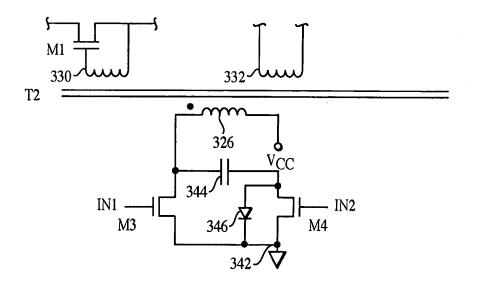


FIG. 44

Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER

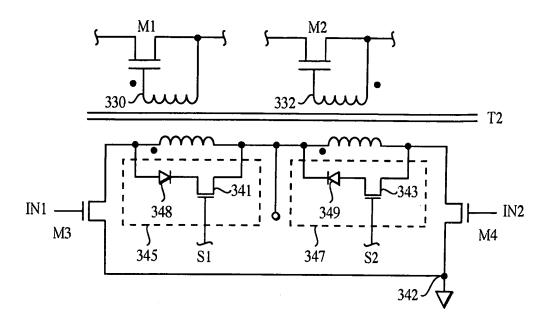
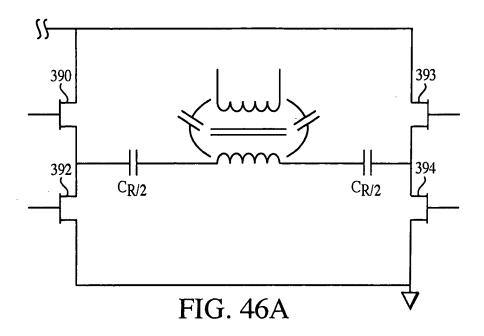
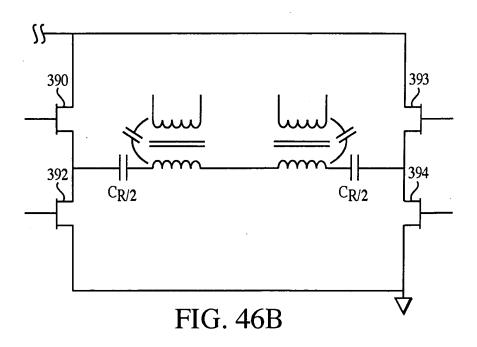
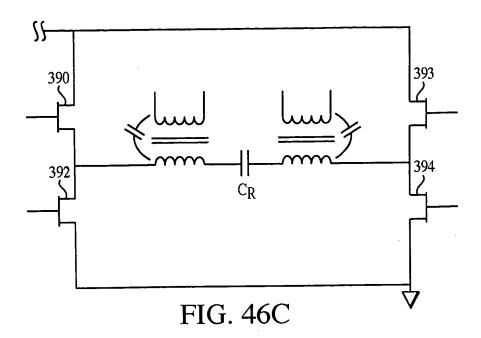


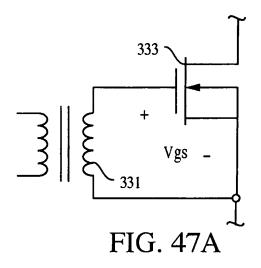
FIG. 45

Applicant(s): Patrizio Vinciarelli
OUTPUT RESISTANCE MODULATION IN POWER
CONVERTERS









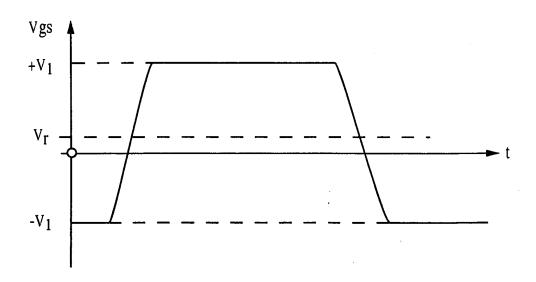


FIG. 47B

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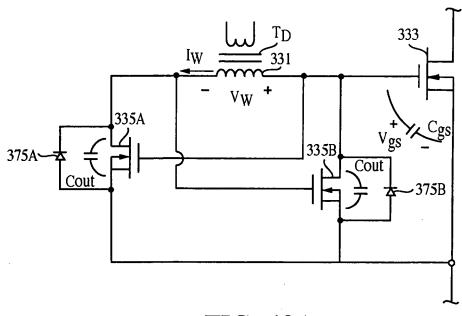
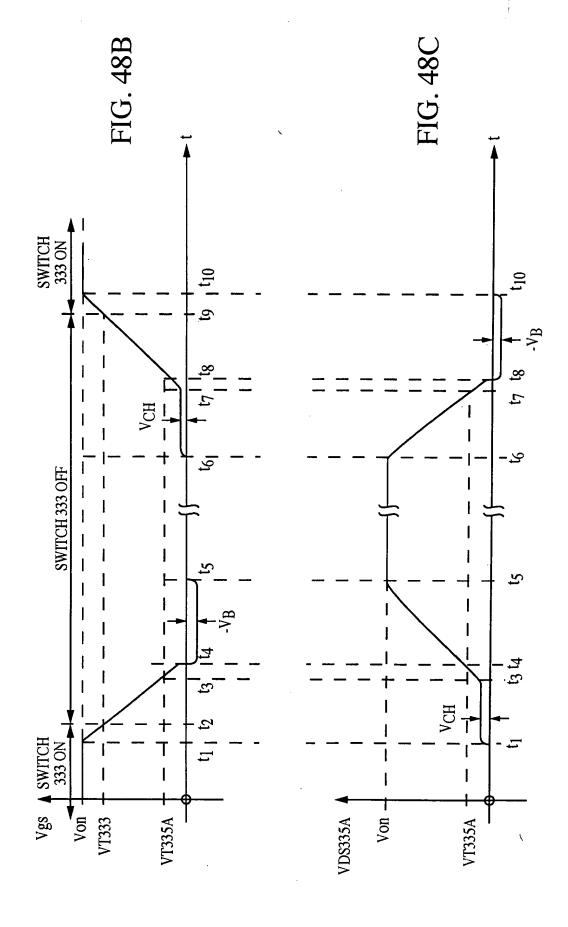
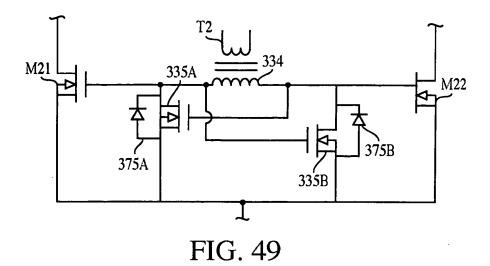
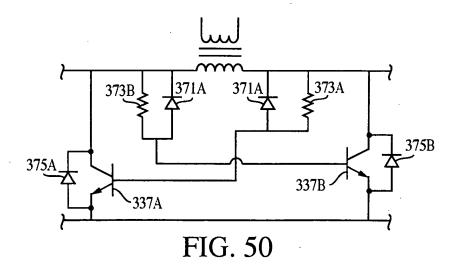


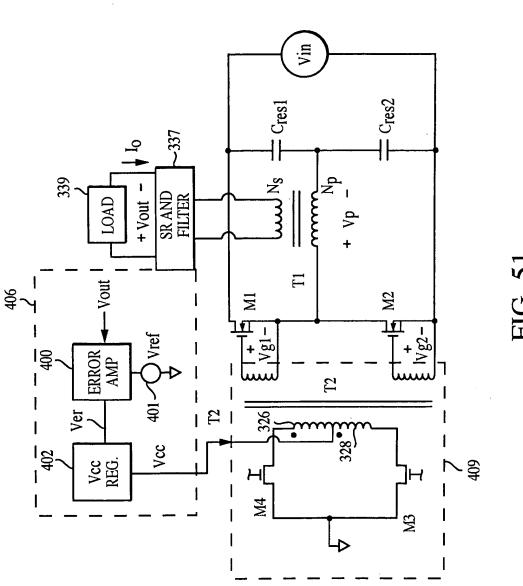
FIG. 48A

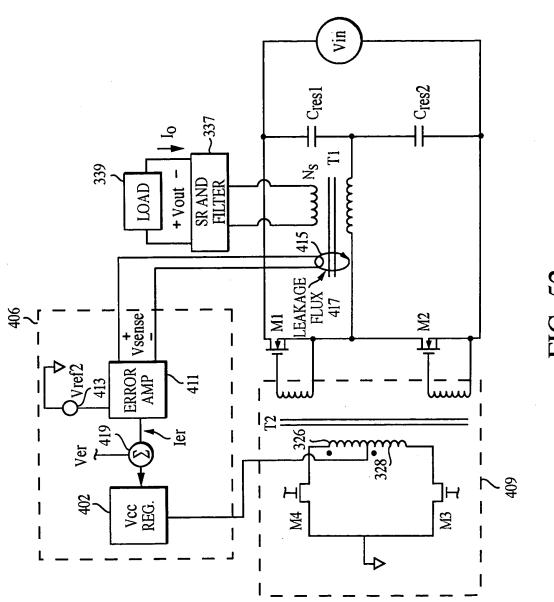


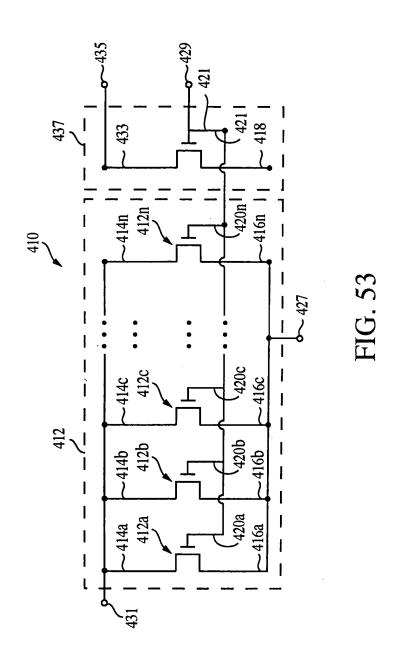


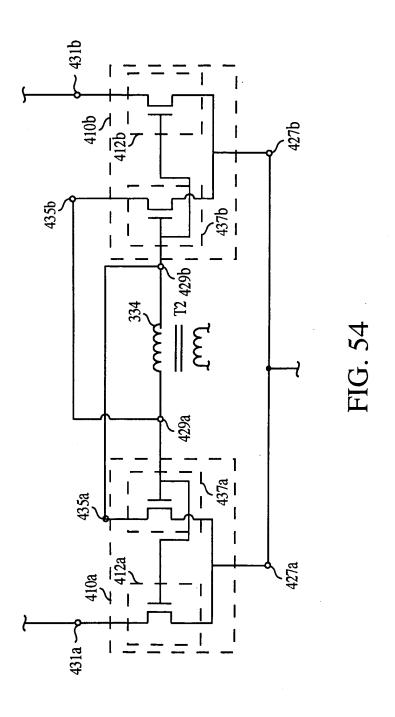


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Matter No.: 00614-136004 Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

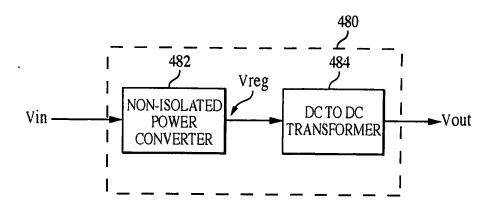


FIG. 55A

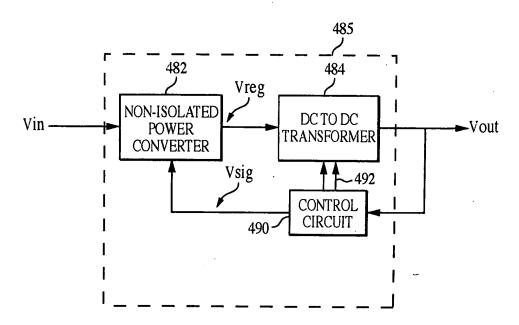


FIG. 55B

Matter No.: 00614-136004 Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

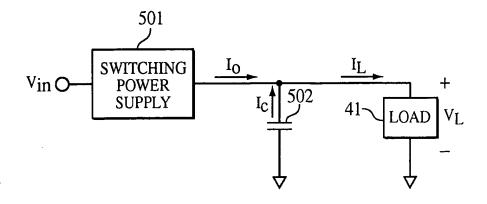


FIG. 56

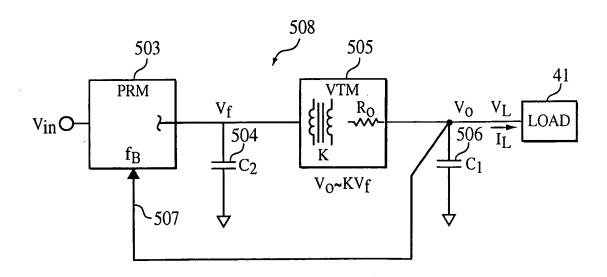


FIG. 58

Applicant(s): Patrizio Vinciarelli

OUTPUT RESISTANCE MODULATION IN POWER

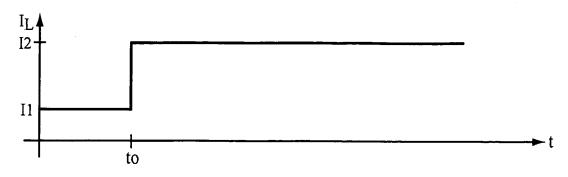


FIG. 57A

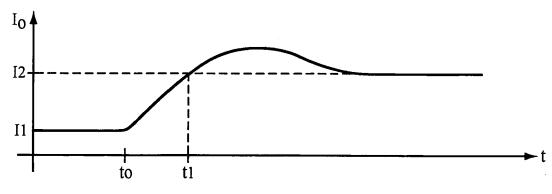


FIG. 57B

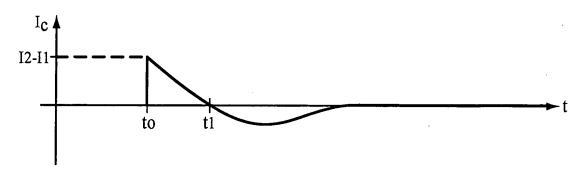


FIG. 57C



FIG. 57D

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OUTPUT RESISTANCE MODULATION IN POWER CONVERTERS

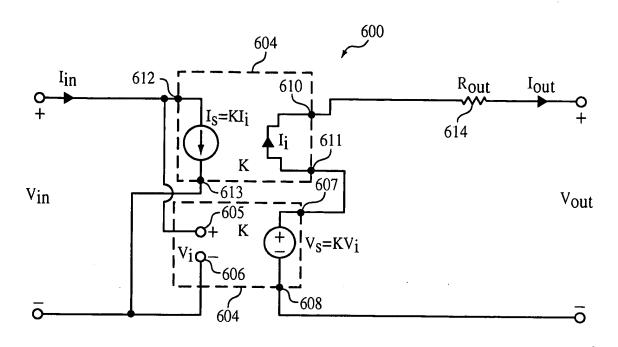


FIG. 59A

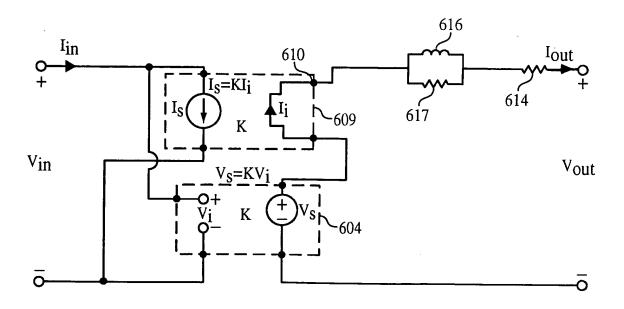


FIG. 59B

